Notice of References Cited

Application/Control No. 10/716,209	Reexamination	Applicant(s)/Patent Under Reexamination LINGUNIS ET AL.		
Examiner	Art Unit			
Asok K. Sarkar	2829	Page 1 of 1		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			-
	С	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	κ	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N		1			
	0					
	Р					·
	Q			·		
	R					
	s					·]
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	Huang et al., "Very Low Defects and High Performance Ge – On – Insulator p – MOSFETs with Al2O3 Gate Dielectrics", 2003 Symposium on VLSI Technology Digest of technical papers, p 119 – 120 (10 – 12 June, 2003).		
	V			
	w			
	х			

"A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.